

FIG. 1

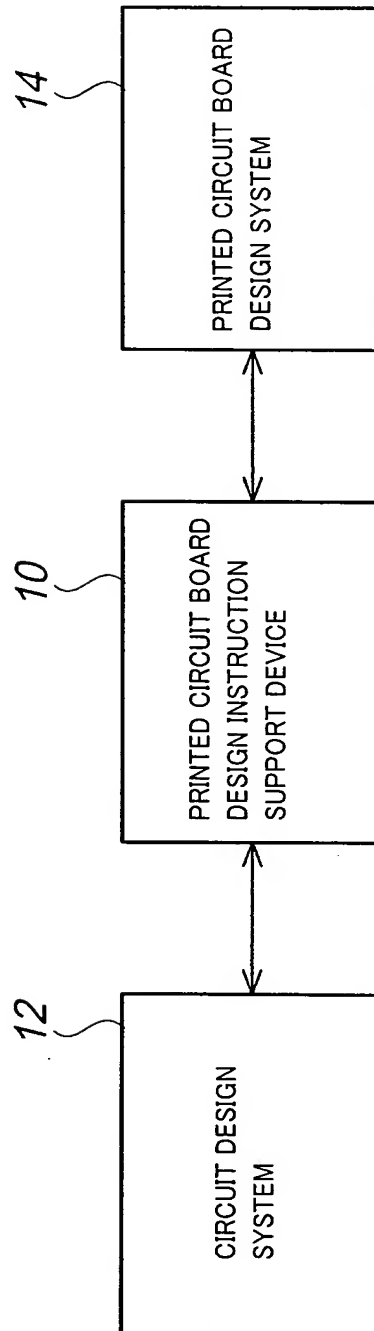


FIG. 2

PRINTED CIRCUIT BOARD DESIGN INSTRUCTION SUPPORT DEVICE									
FILE (E) DISPLAY (V) COMMUNICATION (X) HELP (H)									
				CIRCUIT BOARD DESIGN SIDE (1)		PCB-DR RECORD (1)			
No.	DESIGN INSTRUCTION	KEYWORD	ITEM	BEFORE DR	COMMENT BEFORE DR	CHECK RESULT	COMMENT	ASSOCIATED FILE	
▶ 1	① 接層はG	≡ CLOCK NET	□						
			AY_CHCLK						
			GUARD_CL						
			AAAUDDAT2						
			GUARD_DA						
			BLUE						
			SG00148						
2	スタブ	≡ CLOCK NET	⊕						
3	と最短距	⑤ BYPASS CAPACITOR	⊕						

FIG. 3(a)

SPECIFY DATE		
2003	2	3
OK		Cancel

FIG. 3(b)

PERSON IN CHARGE OF IMPLEMENTATION	
TANAKA	▼
ZUKEN TARO	

FIG. 3(c)

CHECK RESULT	
	▼
OK	
NG	

FIG. 4(a)

CHECK DATE	PERSON IN CHARGE OF CHECK	CHECK RESULT
2003/ 3/25	zuken	OK

FIG. 4(b)

CHECK RESULT
<div></div>
OK
CHECK AGAIN
RECONSIDERATION REQUIRED

FIG. 5(a)

CHECK RESULT	CHECK RESULT
OK:2/6	RECONSIDERATION REQUIRED:3/6
OK	RECONSIDERATION REQUIRED
CHECK AGAIN	RECONSIDERATION REQUIRED
OK	RECONSIDERATION REQUIRED

(a)

FIG. 5(b)

ITEM	CHECK RESULT
<input type="checkbox"/>	
AY_CHCLK	OK
GUARD_CL	CHECK AGAIN RECONSIDERATION REQUIRED
AA_AUDDAT2	
GUARD_DA	
BLUE	
SG00148	

(b)

ITEM	CHECK RESULT
<input type="checkbox"/>	OK:6/6
AY_CHCLK	OK
GUARD_CL	OK
AA_AUDDAT2	OK
GUARD_DA	OK
BLUE	OK
SG00148	OK

FIG. 6

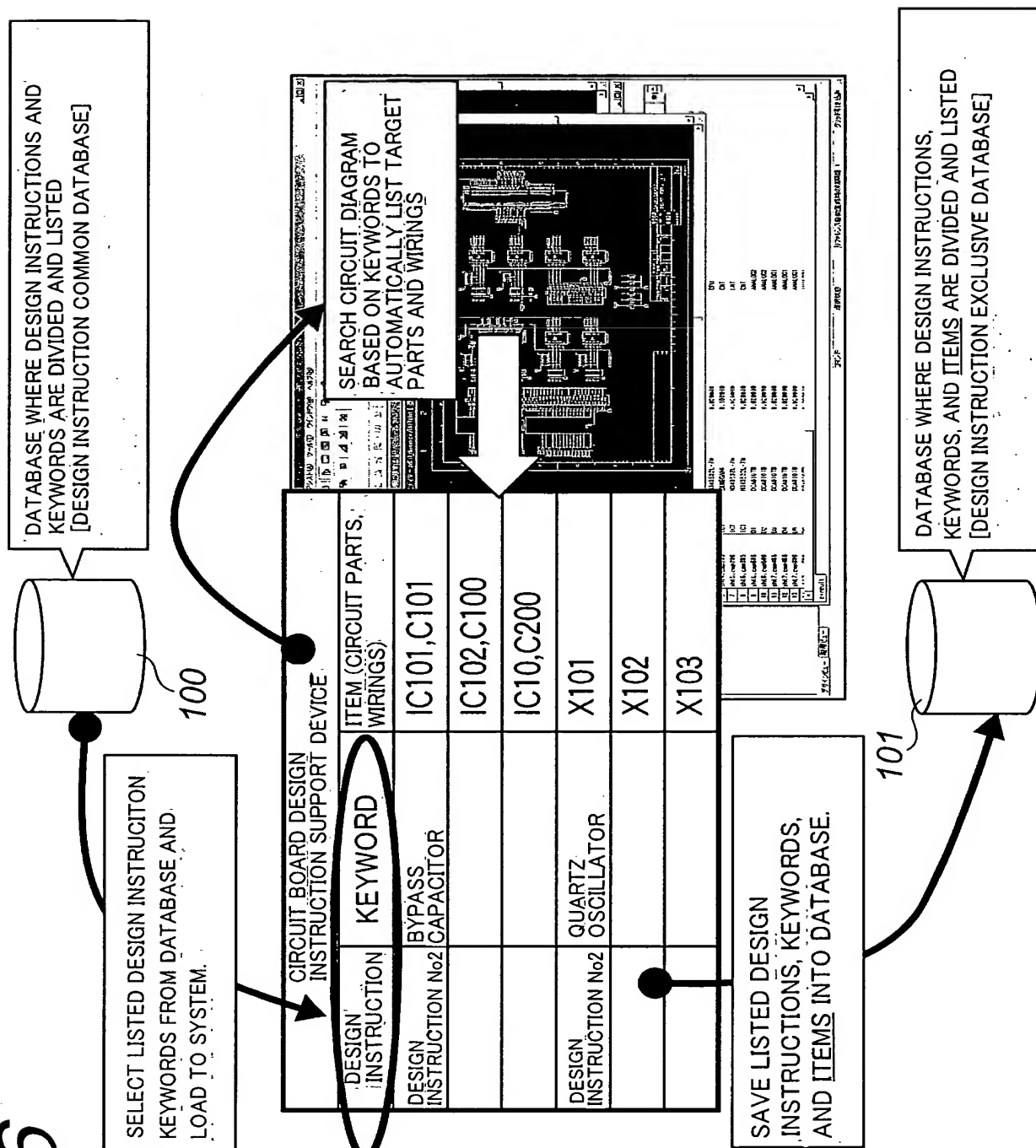


FIG. 7

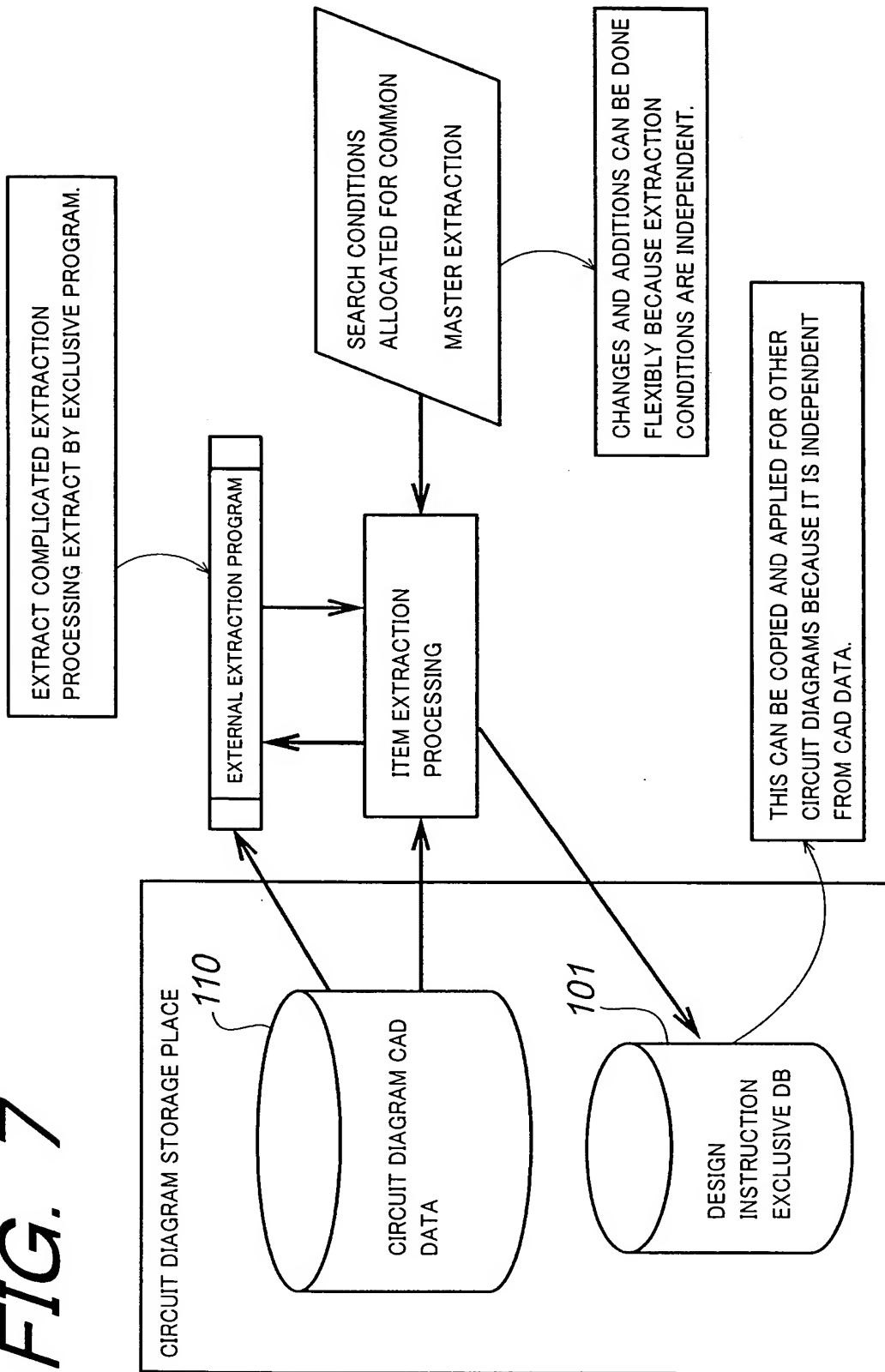


FIG. 8

EXAMPLE)

DESIGN INSTRUCTION

DESIGN INSTRUCTION	KEYWORD	ITEM
SET DESIGN INSTRUCTION FOR CLOCK LINE WIRING	CLOCK LINE	CLK001,CLK002 ...

EXTRACTION CONDITION

KEYWORD	EXTRACTION CONDITION
CLOCK LINE	SUCH AS: WIRINGS WHOSE WIRING NAME BEGINS WITH CLK, OR...

FIG. 9

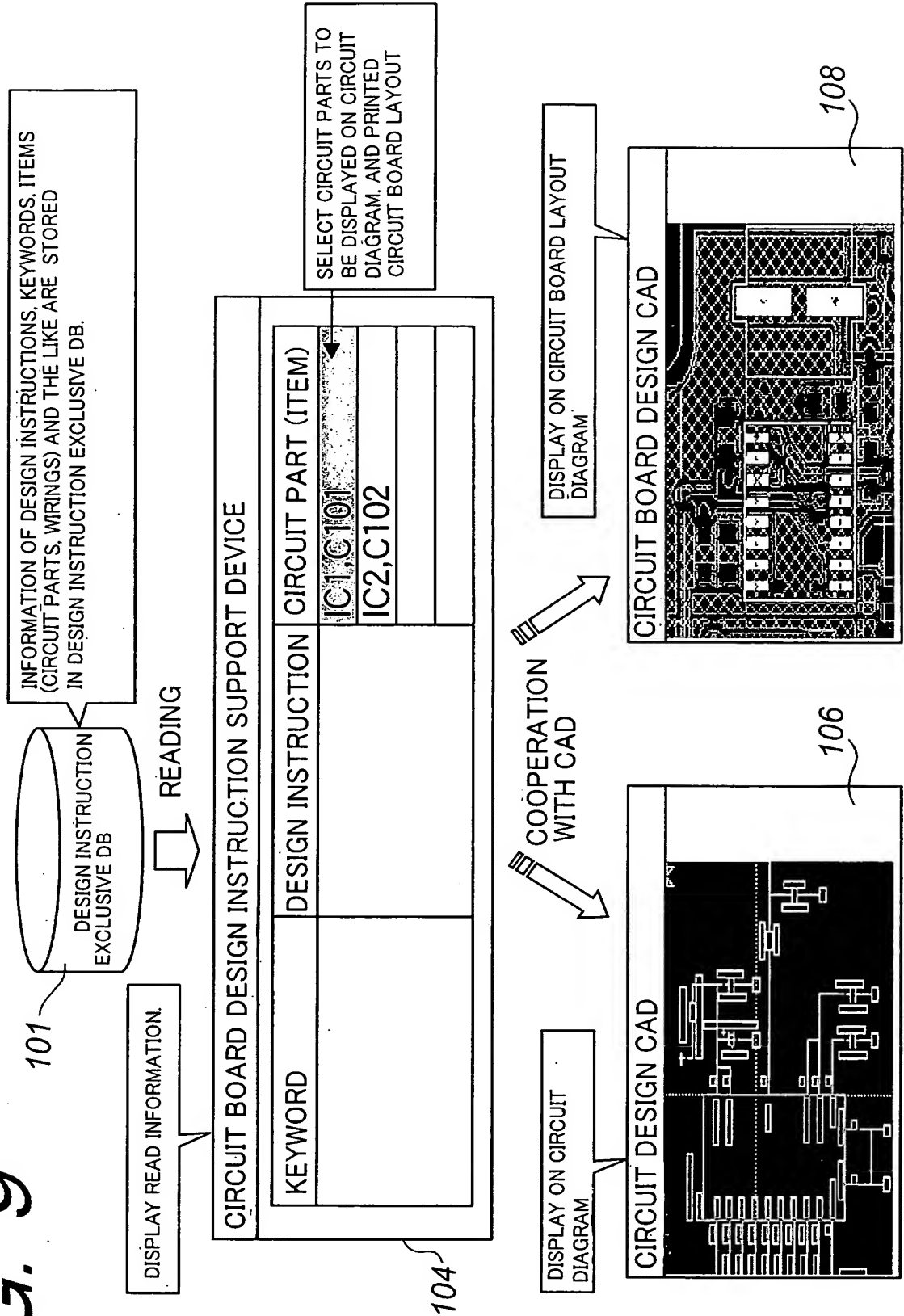
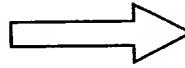
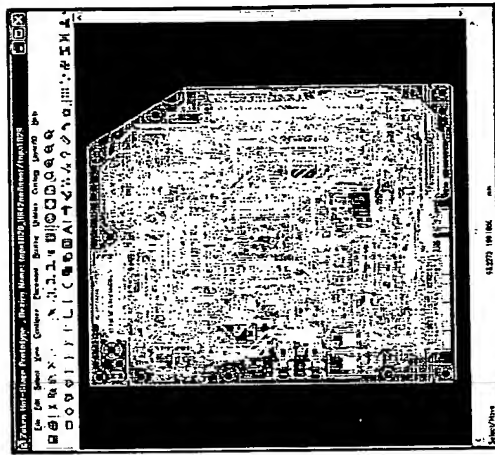
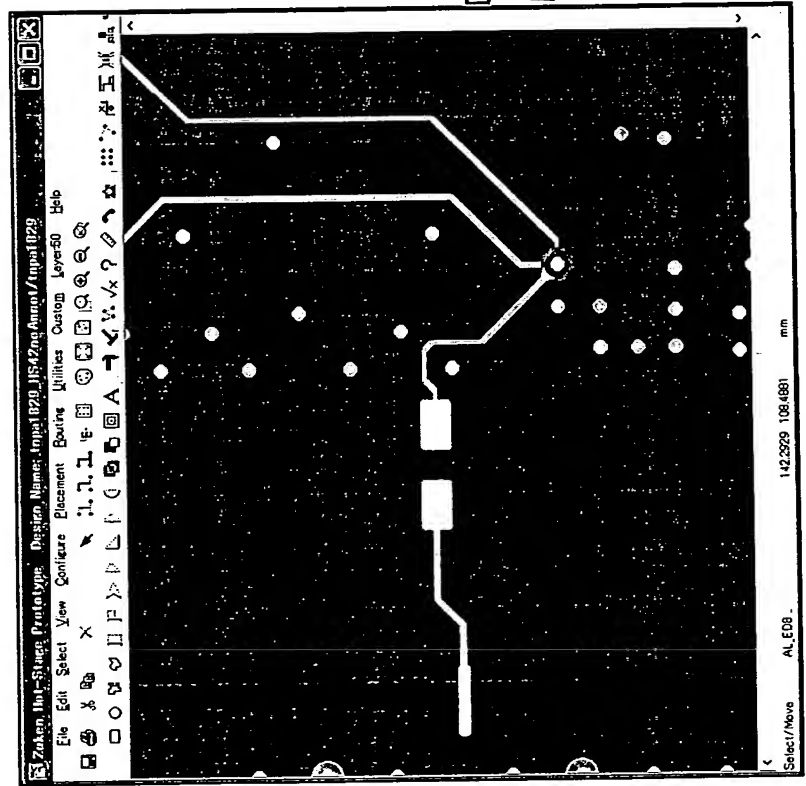


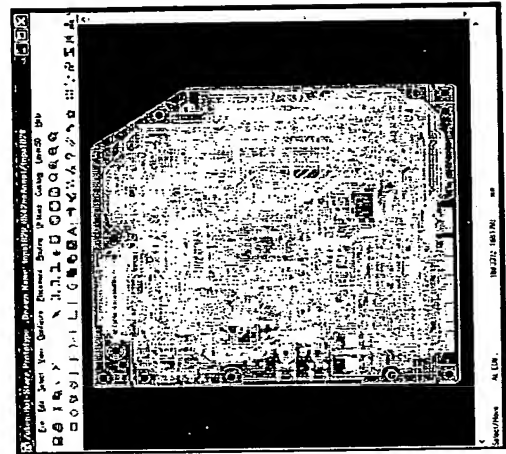
FIG. 10

CHECK OF DRAWING AROUND
 WIRINGS
 "Net1" – "GND"

Pre Script
 [FULL-SCREEN DISPLAY/
 ALL-LAYERS DISPLAY]



[SELECTING ITEMS TO BE HIGHLIGHTED]



Post Script
 [SPECIFYING
 DISPLAY OF LAYER
 TO BE CHECKED/
 REGION]



FIG. 11 (a)

AA_AUDDATA	
GUARD D	CUT(T) COPY(C) PASTE(P)
BLUE	
SG00148	SEE (CIRCUIT DIAGRAM & CIRCUIT BOARD)

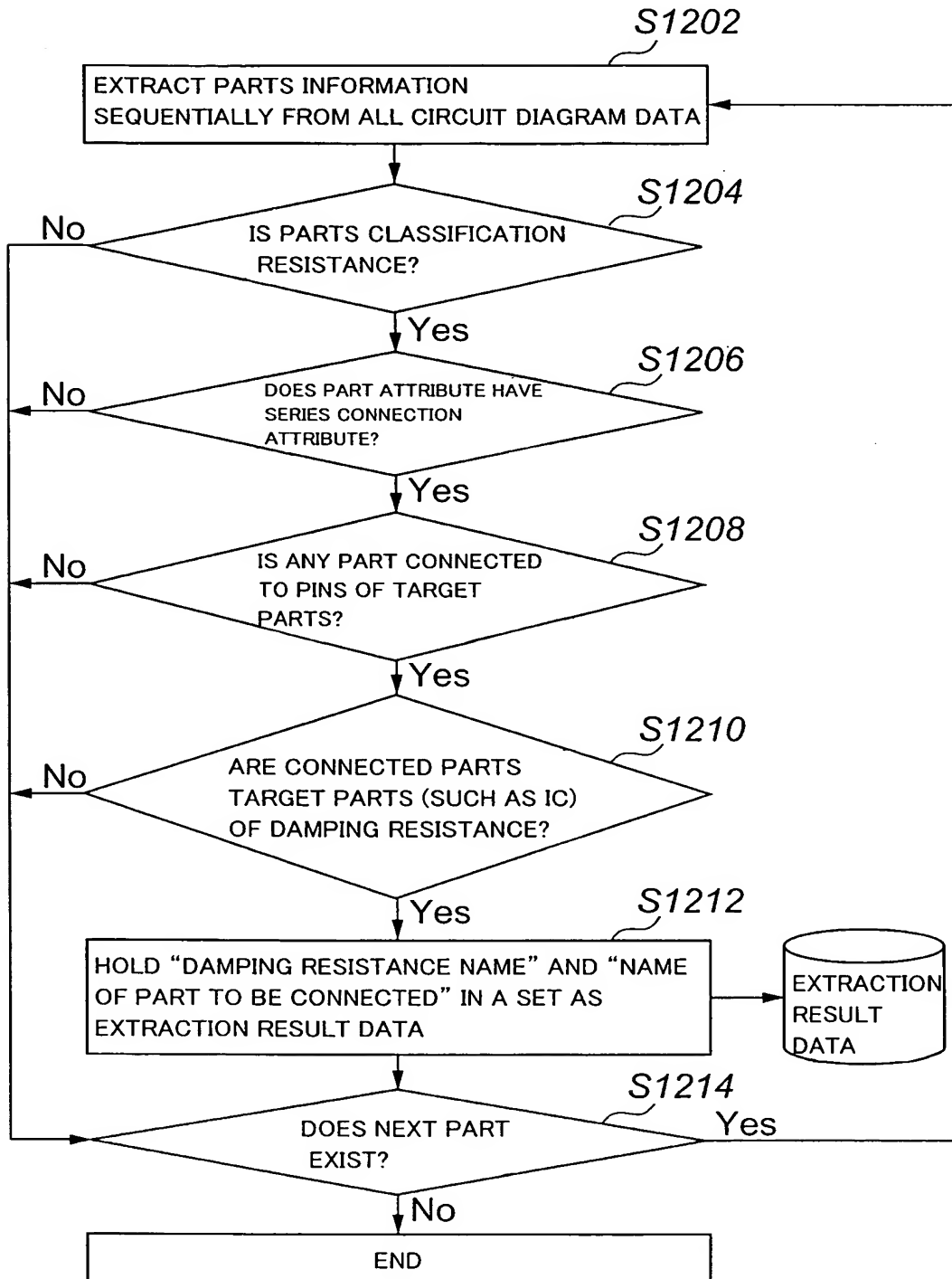
FIG. 11 (b)

COMMUNICATION(X)	
MODE (CIRCUIT DIAGRAM) ▶	<input checked="" type="radio"/> ADDITIONAL SELECTION
MACRO EXECUTION ▶	<input type="radio"/> INDIVIDUAL SELECTION

FIG. 11 (c)

COMMUNICATION(X)	
MODE (CIRCUIT DIAGRAM) ▶	
MACRO EXECUTION	<input checked="" type="checkbox"/> PRE-PROCESSING
	<input checked="" type="checkbox"/> POST -PROCESSING

FIG. 12



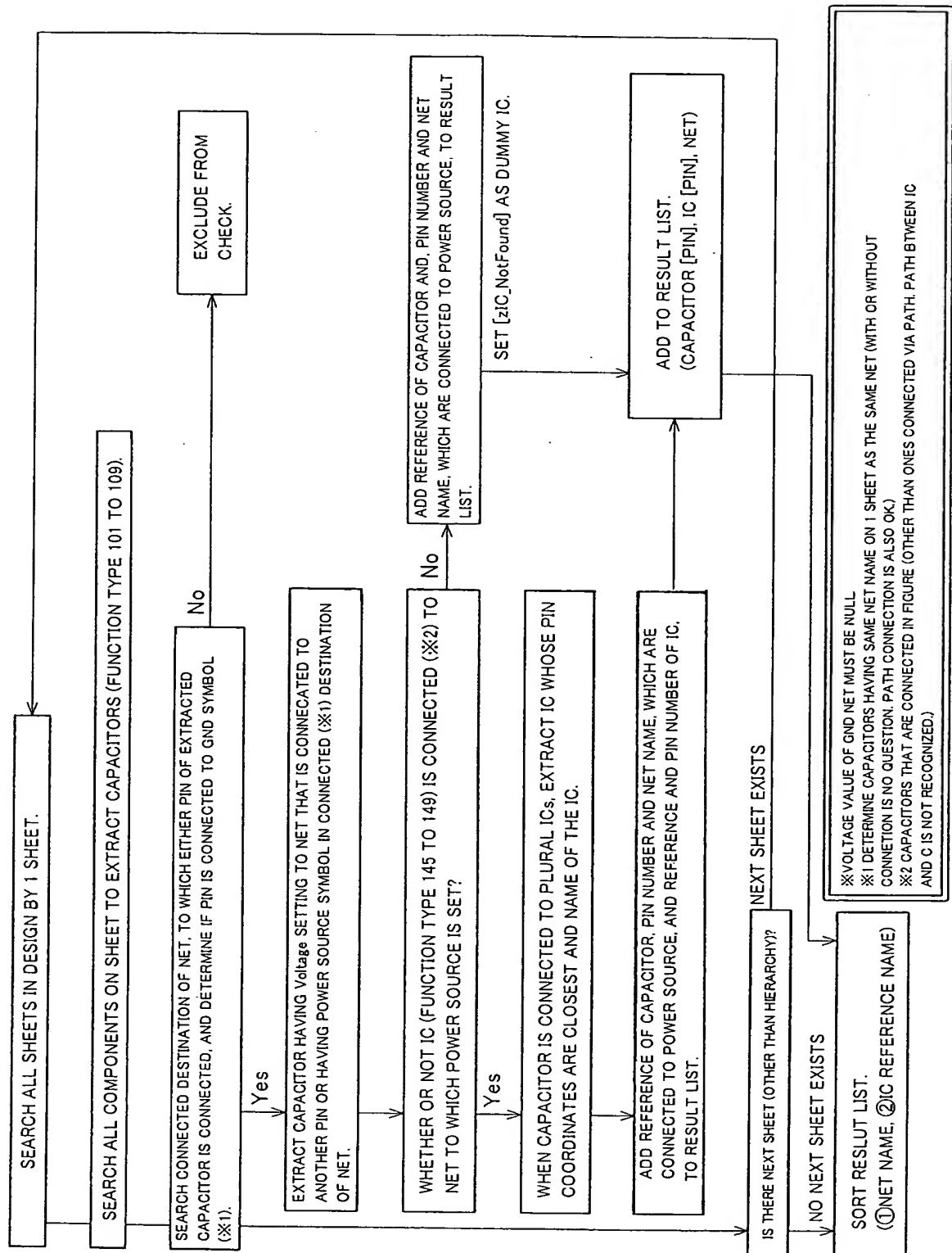


FIG. 14

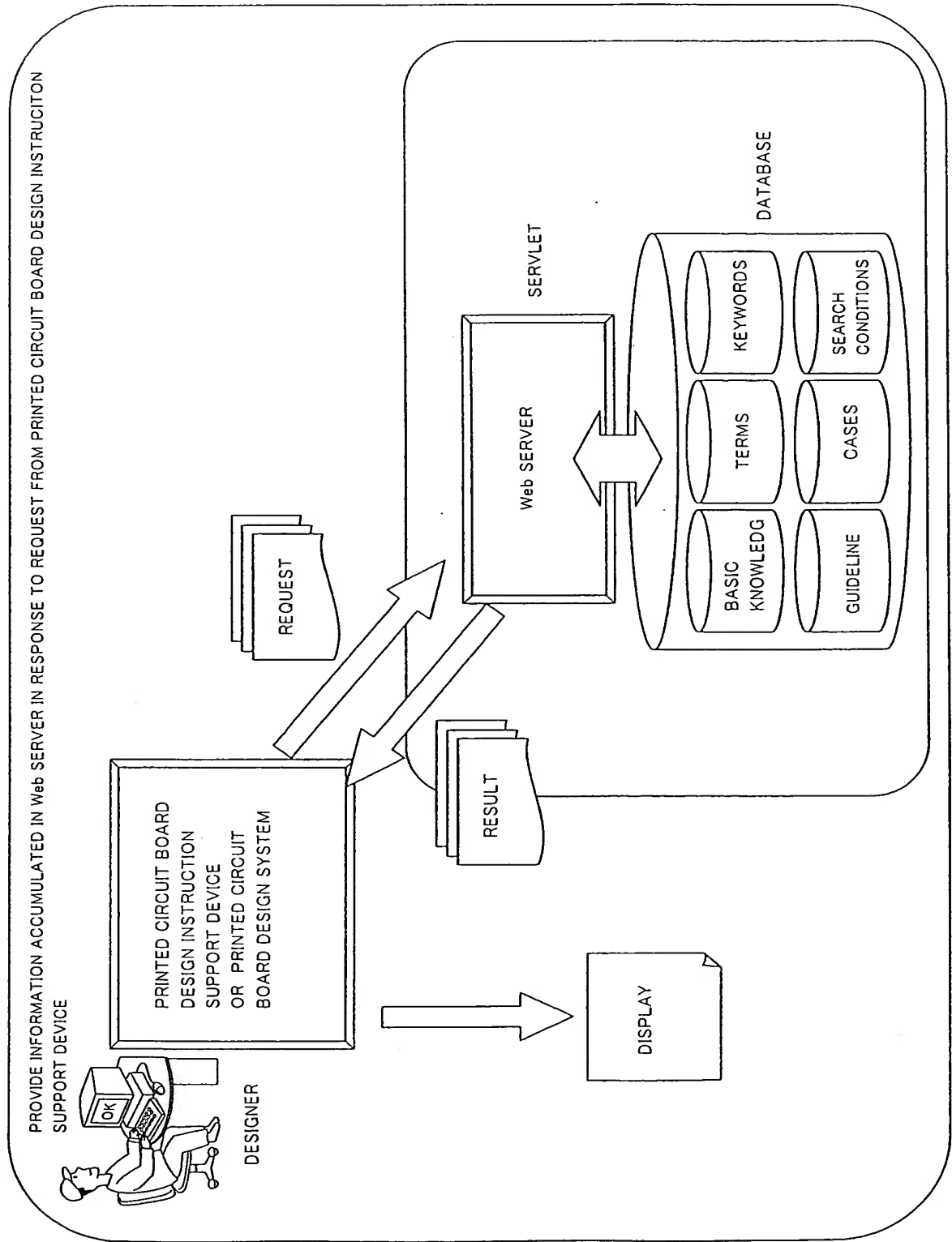


FIG. 15

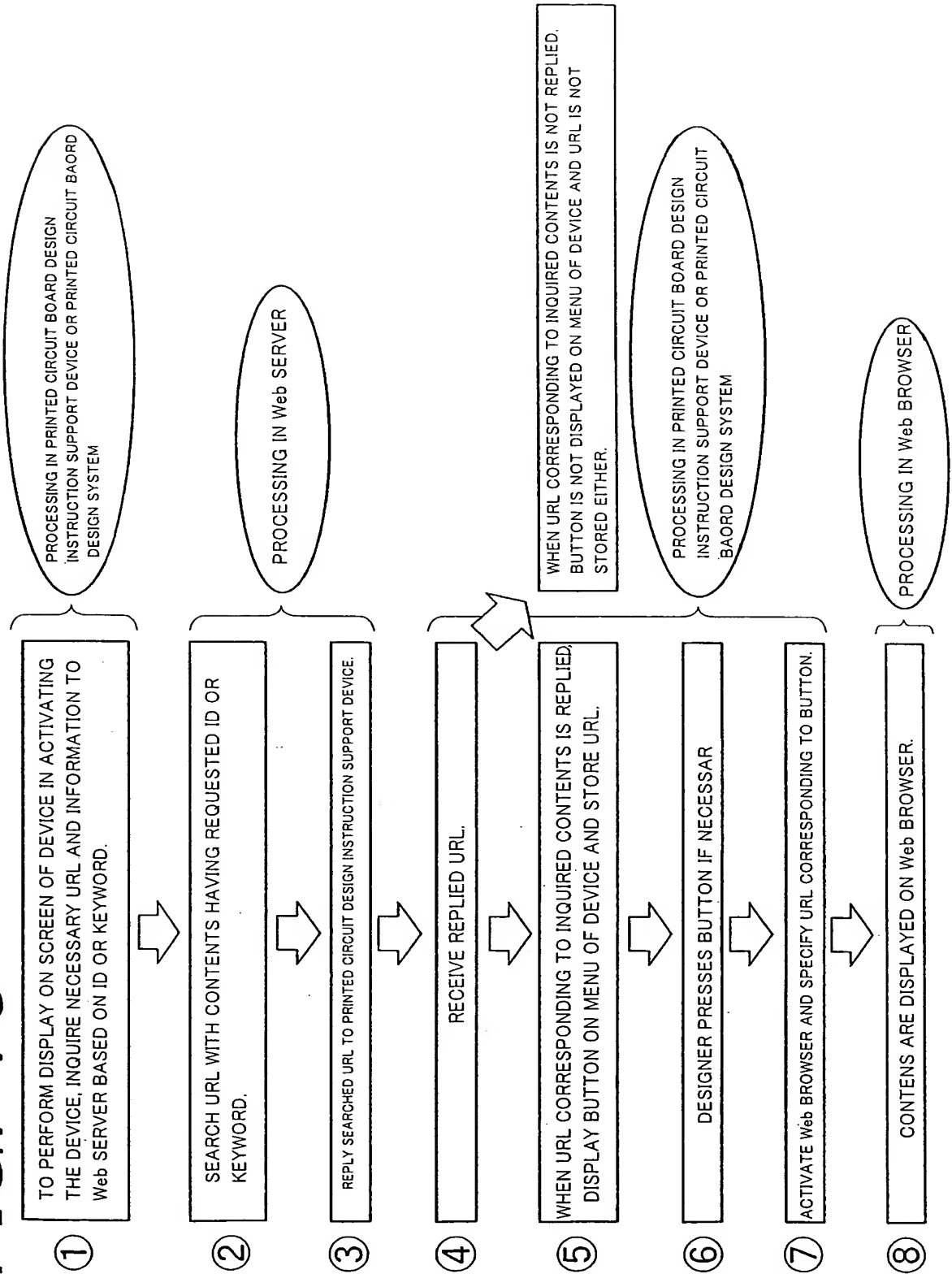


FIG. 16

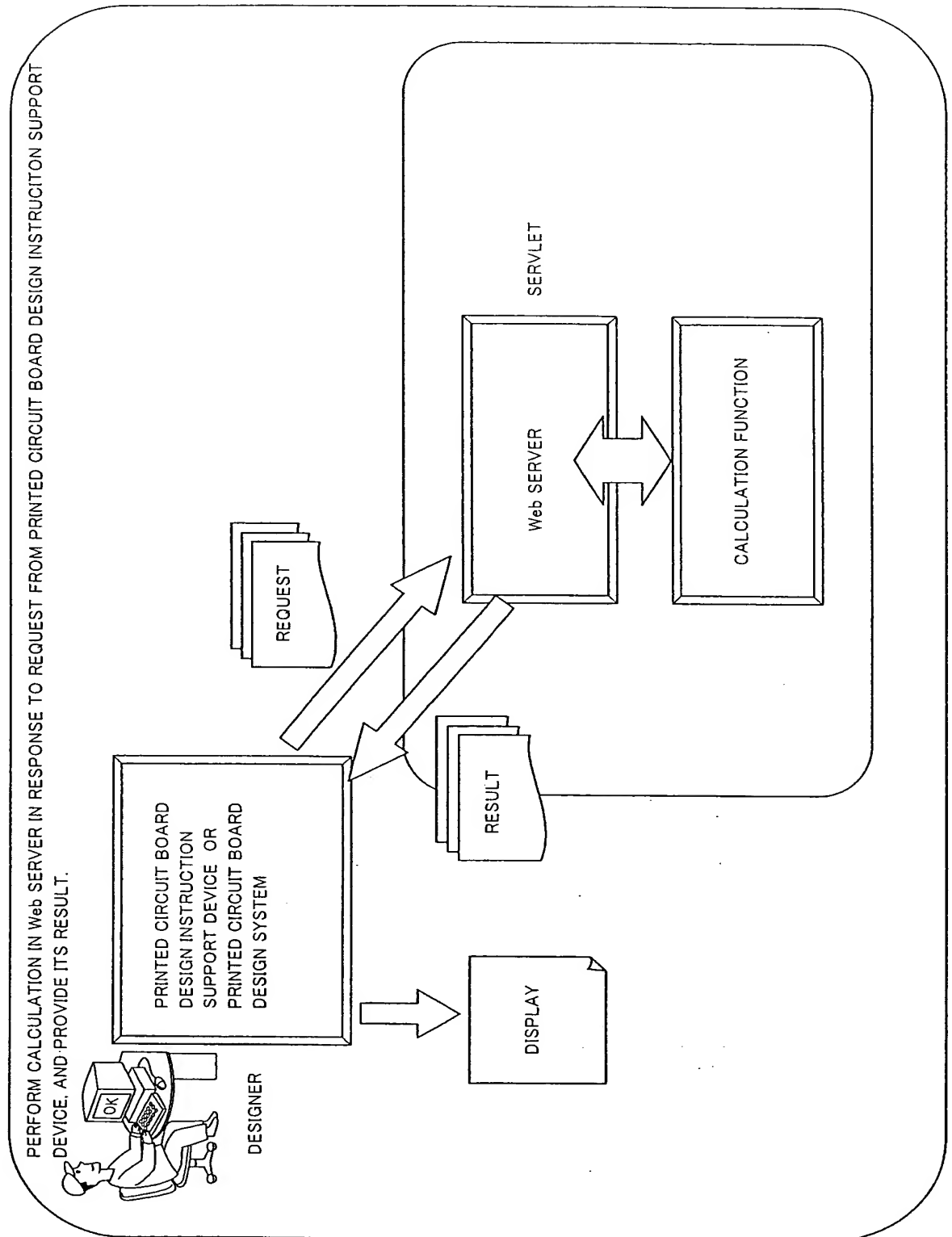


FIG. 17

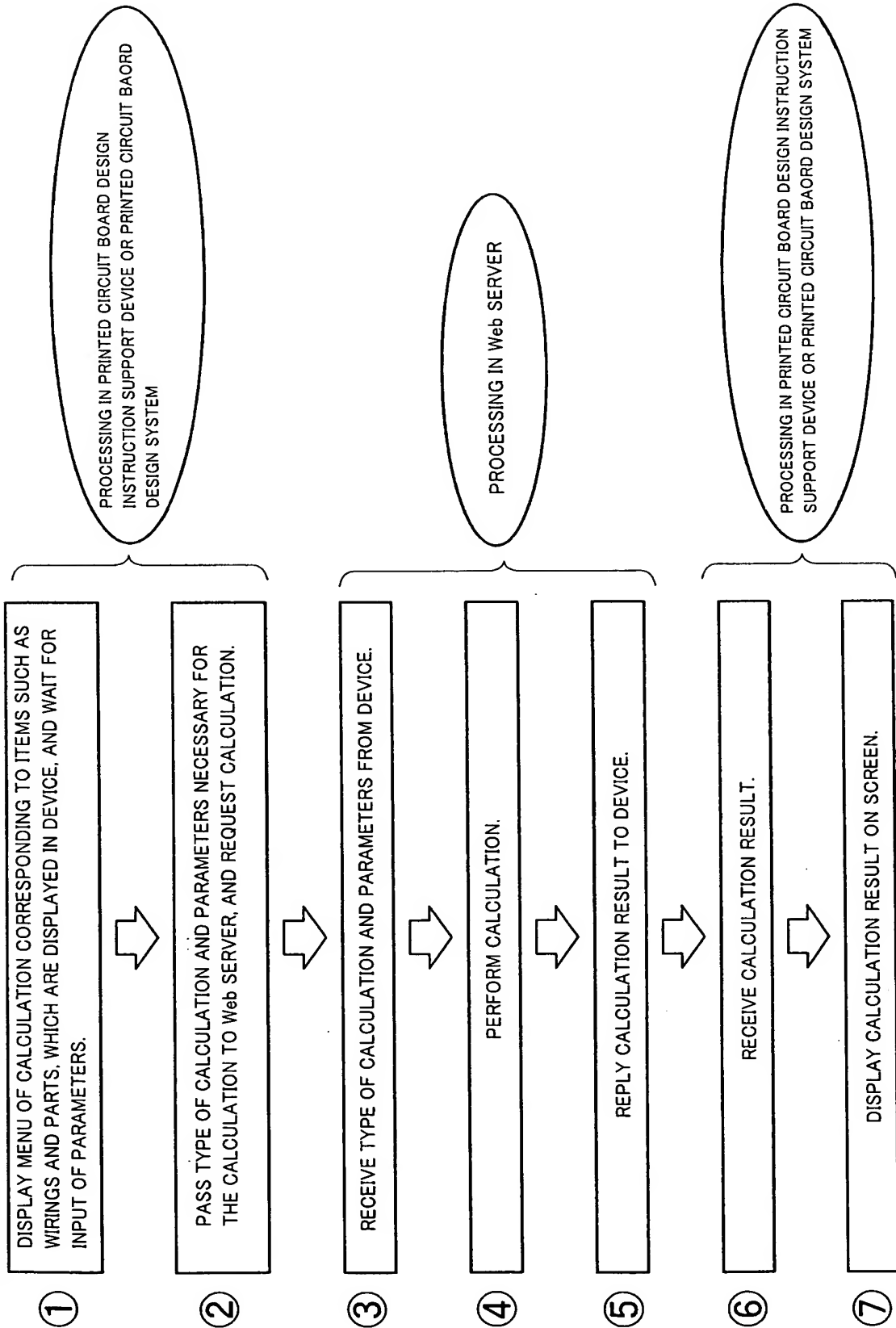


FIG. 18

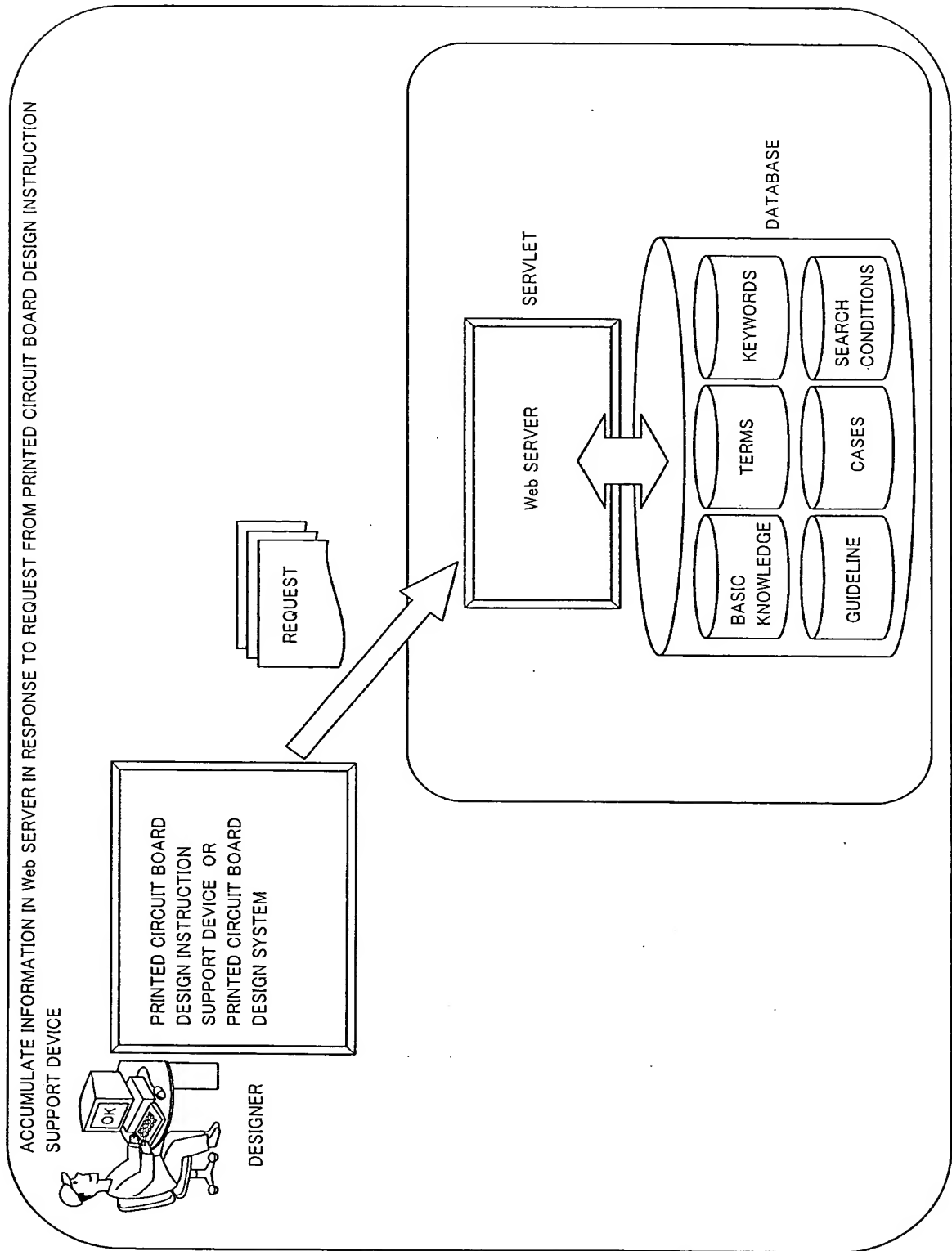


FIG. 19

